/\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*PINOUT CONFIGURATION\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

Digital In: 6, 7, 8, 9, 10, 11, 12, 13 (%IX0.0 - %IX0.7)

Digital Out: 21, 20, 19, 18, 17, 16, 15, 14 (%QX0.0 - %QX0.7)

Analog In: A1, A2, A3 (26,27,28) (%IW0 - %IW2)

Analog Out: 5, 4 (%QW0 - %QW1)

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*/

1-Wire serial protocol is on GPIO22

I2C SDA/UART Tx/SPI Rx on GPIO0

I2C SCL/UART Rx/SPI CS on GPIO1

SPI SCK on GPIO2

SPI Tx on GPIO3

What of “dual purpose” pins (eg analogue outputs can also be configured as digital outputs, SPI SCK and SPI Tx as digital inputs